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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s):	H.S. Nagesh et al.
Case:	10-26-12-10-40
Serial No.:	10/785,352
Filing Date:	February 24, 2004
Group:	2661
Examiner:	To Be Assigned

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature: _____ Date: December 23, 2004

Title: Load Balancing Method and Apparatus for Ethernet Over SONET and Other Types of Networks

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, Applicants' attorney wishes to bring to the attention of the Patent and Trademark Office the following documents listed on the accompanying Form PTO-1449. A copy of each listed document is enclosed.

Other Documents

1. I. Keslassy et al., "Maintaining Packet Order in Two-Stage Switches," Proceedings of IEEE Infocom, 10 pages, New York, 2002.

2. E. Blanton et al., "On Making TCP More Robust to Packet Reordering," pp. 1-11, July 24, 2001.

3. S. Iyer et al., "Making Parallel Packet Switches Practical," Proceedings of IEEE Infocom, 8 pages, 2001.

4. C-S. Chang et al., "Load Balanced Birkhoff-Von Neumann Switches, Part II: Multi-Stage Buffering," pp. 1-23, August 29, 2001.

It is believed that there is no fee due in conjunction with the filing of this Information Disclosure Statement. In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **Ryan, Mason & Lewis, LLP Deposit Account No. 50-0762** as required to correct the error.

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, or as an admission that the information cited is considered to be material to patentability, or as a representation that no other material information exists.
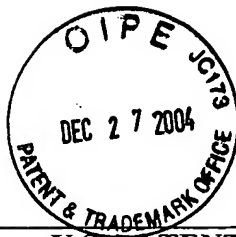
Respectfully submitted,

Date: December 23, 2004

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Applicant(s): H.S. Nagesh et al.
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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	DATE	NAME	CLASS/SUBCLASS	FILING DATE IF APPROPRIATE
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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	DATE	COUNTRY	CLASS/SUBCLASS	TRANSLATION YES NO

OTHER DOCUMENTS

EXAMINER INITIAL	REF NO.	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.

____ 1. I. Keslassy et al., "Maintaining Packet Order in Two-Stage Switches," Proceedings of IEEE Infocom, 10 pages, New York, 2002.

____ 2. E. Blanton et al., "On Making TCP More Robust to Packet Reordering," pp. 1-11, July 24, 2001.

____ 3. S. Iyer et al., "Making Parallel Packet Switches Practical," Proceedings of IEEE Infocom, 8 pages, 2001.

____ 4. C-S. Chang et al., "Load Balanced Birkhoff-Von Neumann Switches, Part II: Multi-Stage Buffering," pp. 1-23, August 29, 2001.

Examiner _____   Date Considered _____

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

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